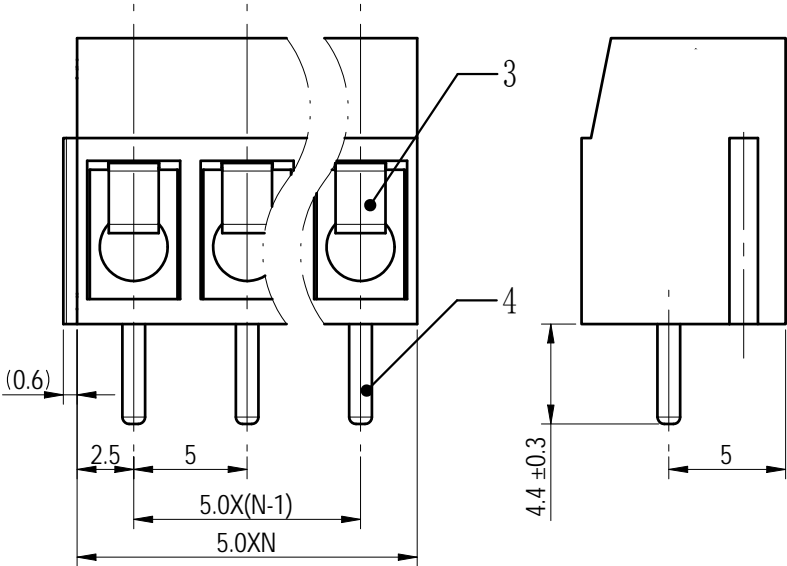
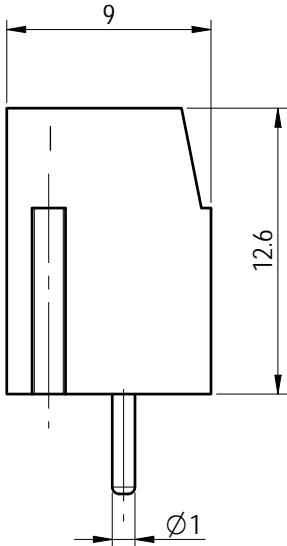
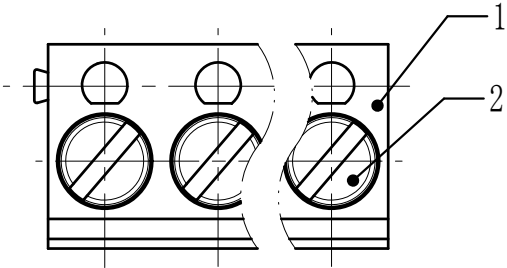
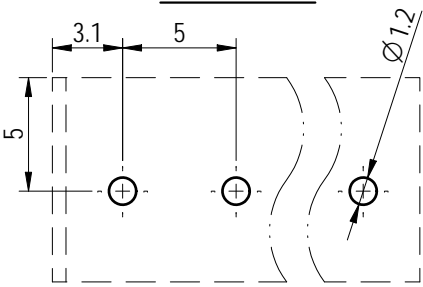
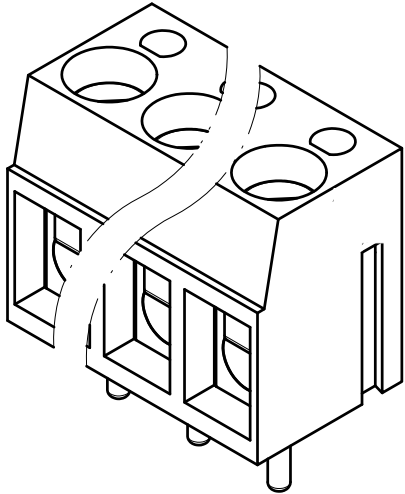


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PCB LAYOUT



未注公差/General tolerances (mm)														0.1/10	
0	>3	>6	>10	>18	>30	>50	>80	>120	>180	>250	>315	>400	>500	±2°	0.1/10
~3	~6	~10	~18	~30	~50	~80	~120	~180	~250	~315	~400	~500			
±0.12	±0.18	±0.22	±0.26	±0.31	±0.37	±0.57	±0.80	±0.93	±1.05	±1.15	±1.60	±2.20	±2.50		



A

B

C

D